SHEET 1 OF 5 ATTY, DOCKET NO. SERIAL NO. FORM PTO-1449 U.S. Department of Commerce(Rev. 4/92) Patent and Trademark 501.35478CX23 Not yet assigned Office INFORMATION DISCLOSURE APPLICANT STATEMENT BY APPLICANT MIWA, et al (Use several sheets if necessary) GROUP FILING DATE March 29, 2004 2818 **U.S. PATENT DOCUMENTS** EXAMINER INITIAL DATE DOCUMENT NUMBER 185.03 365 us 9 10/90 Devin 6 36 S 185,03 5 6 5 4 8 6 11/94 Schreck 5 4 2 0 5/95 Sawada et al 1 6 1 365 185.03 3 9 0 0 11/98 5 8 Miyamoto 365 185.03 7 4/98 185.03 5 4 5 0 9 Wong et al 365 5 5 5 5 2 0 4 9/96 Endoh et al 365 189.01 9/92 5 5 3 8 7 Brady et al 437 191 4/30/ 7 8 1/92 192 3/13/ Joshi et al 437 5 5 10/91 437 193 6/23/ 5 0 9 5 4 Spinner et al 185,03 365 12/92 7 2 3 3 8 Mehrotra et al 5 185,03 365 5 1 3 8 5 7 6 8/92 Madurawe 8 3 5 0 6 1/96 365 185.23 Akaogi et al FOREIGN PATENT DOCUMENTS DOCUMENT NUMBER CLASS SUBCLASS DATE COUNTRY ABSTRA YES 5 6 9 6 7/84 9 Japan 3 8/92 Japan 9 2/92 Japan OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Digest of Technical Papers, ATP 2.1: A 3.3V 128Mb Multi-level NAND Flash Memory for Mass Stora a~ 6 Applications, T. Jung, et al., ISSCC98/Session 2/Flash Memory/Paper TP 2.1, 1996 EEE Internation State Circuits Conf. Betty Prince, "Semiconductor Memories", 1983, Wisely, 2nd Edition, pp. 620-621. **DATE CONSIDERED EXAMINER** O

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SHEET 2 OF 5 FORM PTO-1449 U.S. Department of SERIAL NO. ATTY. DOCKET NO. Commerce (Rev. 4/92) Patent and Trademark 501.35478CX23 Not yet assigned Office **APPLICANT** INFORMATION DISCLOSURE MIWA, et al STATEMENT BY APPLICANT **GROUP** FILING DATE (Use several sheets if necessary) March 29, 2004 2818 **U.S. PATENT DOCUMENTS**

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